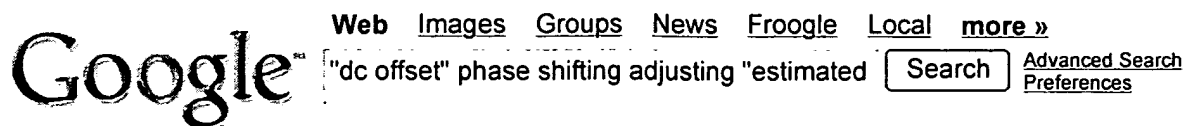


Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	12	("4459699" "5995819" "6148047" "6356218" "6366622" "6678178"). PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 15:38
L2	3317	phase with shift\$2 with (LPF or (low adj pass adj filter))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 15:41
L3	2917	(phase adj shift\$2) with (LPF or (low adj pass adj filter))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 15:50
L4	920679	(DC or (direct adj current) near offset) (phase adj shift\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 15:51
L5	3099	(DC or (direct adj current) near offset) with (phase adj shift\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 15:51
L6	161	(DC or (direct adj current) near offset) with adjust\$3 with(phase adj shift\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 15:51
L7	161	(DC or (direct adj current) near offset) with adjust\$3 with (phase adj shift\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:02
L8	261	(DC or (direct adj current) near offset) with (adjust\$3 or correct\$3) with (phase adj shift\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:19

L9	3	(DC or (direct adj current) near offset) with (adjust\$3 or correct\$3) with (phase adj shift\$2) with estimat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:06
L10	6	(4385328 4459699 6108696).pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:06
L11	12	(DC or (direct adj current) near offset) with (adjust\$3 or correct\$3) with (phase adj shift\$2) with compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:48
L12	17	(DC or (direct adj current) near offset) with GFSK	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:29
L13	3	(DC or (direct adj current) near offset) with GFSK and (phase with shift\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 16:50
L14	36	(DC or (direct adj current) near offset) with (adjust\$3 or correct\$3) with ((phase adj shift\$2) or delay\$3) with compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:50
L15	3	(DC or (direct adj current) near offset) with GFSK and (delay\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:02
L16	46	Bluetooth and ((DC adj offset) with estimat\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:03

L17	3059	375/346	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:49
L18	1141	(DC or (direct adj current) near offset) with (adjust\$3 or correct\$3) with compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:51
L19	5	17 and 18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:50
L20	162	((DC or (direct adj current) near offset) and (adjust\$3 or correct\$3) and compar\$3 and (phase with shift\$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:52
L21	0	((DC or (direct adj current) near offset) and (adjust\$3 or correct\$3) and compar\$3 and (phase with shift\$3) and (estimat\$3 adj cd adj offset)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:53
L22	0	((DC or (direct adj current) near offset) and (adjust\$3 or correct\$3) and compar\$3 and (phase with shift\$3) and (estimat\$3 with cd adj offset)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:53
L23	10	((DC or (direct adj current) near offset) and (adjust\$3 or correct\$3) and compar\$3 and (phase with shift\$3) and (estimat\$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:54
L24	10	((DC or (direct adj current) adj offset) and (adjust\$3 or correct\$3) and compar\$3 and (phase with shift\$3) and (estimat\$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/09 17:54

S1	1	"10/024720"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 08:41
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CONVERTER COMPENSATION - Patent Storm

... pass filtered and **estimated DC offset** data of the ADC are obtained in ADC DC

... A transmitter circuit (10) includes a **phase shifter** (20) that receives ...

www.patentstorm.us/class/341/118-CONVERTER_COMPENSATION.html - 50k -

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Particular pulse demodulator or detector - Patent Storm

A differential detector generates, for each symbol, a **phase** difference between

... the optical storage system and subtracts an **estimated DC offset** from the. ...

www.patentstorm.us/class/375/340-Particular_pulse_demodulator_or_detector.html - 50k -

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United States Patent Application: 0050164639

The **phase shift** is typically removed utilizing various complex digital signal

... Both gear **shift** logic circuits track this difference and **adjust** their ...

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United States Patent Application: 0050020226

in Order to generate the I and Q signals, a ninety degrees **phase shifter** is ...

This is because the **estimated DC offset** is not the true **DC offset** when the ...

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[PDF] photonics.anu.edu.au/qoptics/publications/2000wu.pdf

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Dc offset estimation and compensation in ofdm radio receivers by ...

... and to a compensation method based on the **estimated Dc offset**. ... the maximum

permissible Frequency **shift** is ± 250 kHz, with FIG. ...

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Band-limited adaptive feedback canceller for hearing aids patent

[0034] When the above **Phase** condition is met and the Magnitude of the ... the **DC**

offset in the filter coefficient and subtract the **estimated DC offset** from ...

www.freshpatents.com/Band-limited-adaptive-feedback-canceller-for-hearing-aids-

dt20050728ptan20050163331.... - 63k - Supplemental Result - [Cached](#) - [Similar pages](#) - [Remove result](#)

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[PDF] Prediction of CT Saturation Period for Differential Relay ...

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fault case (**shifting** up of the Differential curve or slope ... protected

generator (secondary signals, **phase** L1) are shown ...

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- ☐ 1. **SIGNAL DC OFFSET CORRECTION METHOD AND DEVICE**
SCHETELIG, Markus / BURGESS, Paul / NOKIA CORPORATION, PATENT
COOPERATION TREATY APPLICATION, Jan 2003
...Signal 1) a **phase shifting** element...LPF2) for **adjusting** the **estimated DC offset**
when Signal...corresponds to the **estimated DC offset**. The output...invention the
phase shifting element is...means for **adjusting** the **estimated DC offset** portion
can...

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"dc offset" /
shifting" AN
"estimated
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quotes to c
and by rem
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IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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1-25

- ☐ 1. **Design and Analysis of CMOS Broad-Band Compact High-Linearity Modulators for Gigabit Microwave/Millimeter-Wave Applications**
 Chang, H.-Y.; Wu, P.-S.; Huang, T.-W.; Wang, H.; Chang, C.-L.; Chern, J.G.J.;
[Microwave Theory and Techniques, IEEE Transactions on](#)
 Volume 54, Issue 1, Jan. 2006 Page(s):20 - 30
 Digital Object Identifier 10.1109/TMTT.2005.860900
[AbstractPlus](#) | Full Text: [PDF\(1168 KB\)](#) IEEE JNL
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- ☐ 2. **A low-cost dual-mode noncoherent receiver with robust frequency-offset compens**
 Liu, C.-L.; Djen, W.S.; Feher, K.;
[Vehicular Technology Conference, 1993 IEEE 43rd](#)
 18-20 May 1993 Page(s):412 - 415
 Digital Object Identifier 10.1109/VETEC.1993.507499
[AbstractPlus](#) | Full Text: [PDF\(332 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **An I/Q active balanced harmonic mixer with IM2 cancelers and a 45° phase shifter**
 Yamaji, T.; Tanimoto, H.; Kokatsu, H.;
[Solid-State Circuits, IEEE Journal of](#)
 Volume 33, Issue 12, Dec. 1998 Page(s):2240 - 2246
 Digital Object Identifier 10.1109/4.735708
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(520 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 4. **Zero-crossing Shapiro step in a three-junction SQUID magnetically coupled with the phase-shifted RF signals**
 Mizugaki, Y.; Jian Chen; Nishikata, S.; Sugi, K.; Nakajima, K.; Yamashita, T.;
[Applied Superconductivity, IEEE Transactions on](#)
 Volume 13, Issue 2, Part 1, June 2003 Page(s):926 - 929
 Digital Object Identifier 10.1109/TASC.2003.814084
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1077 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 5. **Phased array control using phase-locked-loop phase shifters**
 Houghton, A.W.; Brennan, P.V.;
[Microwaves, Antennas and Propagation, IEE Proceedings H](#)
 Volume 139, Issue 1, Feb. 1992 Page(s):31 - 37
[AbstractPlus](#) | Full Text: [PDF\(404 KB\)](#) IEEE JNL

- 6. **Complete elimination of DC offset in current signals for relaying applications**
 Jun-Zhe Yang; Chih-Wen Liu;
Power Engineering Society Winter Meeting, 2000. IEEE
 Volume 3, 23-27 Jan. 2000 Page(s):1933 - 1938 vol.3
 Digital Object Identifier 10.1109/PESW.2000.847649
[AbstractPlus](#) | Full Text: [PDF\(312 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 7. **A new family of measurement technique for tracking voltage phasor, local system frequency, harmonics and DC offset**
 Jun-Zhe Yang; Chih-Wen Liu;
Power Engineering Society Summer Meeting, 2000. IEEE
 Volume 3, 16-20 July 2000 Page(s):1327 - 1332 vol. 3
 Digital Object Identifier 10.1109/PESS.2000.868716
[AbstractPlus](#) | Full Text: [PDF\(424 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 8. **A new phase-feedback digital oscillator for HHFW transmitters**
 Greenough, N.; Lafrance, D.;
Fusion Engineering, 2003. 20th IEEE/NPSS Symposium on
 14-17 Oct. 2003 Page(s):504 - 507
 Digital Object Identifier 10.1109/FUSION.2003.1426694
[AbstractPlus](#) | Full Text: [PDF\(1721 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 9. **A low-noise direct conversion PSK receiver for TDMA land mobile communication**
 Hayashi, R.; Nakajima, T.; Shimozawa, M.; Miyake, M.; Fujino, T.;
Personal, Indoor and Mobile Radio Communications, 1997. 'Waves of the Year 2000'. PI
'97., The 8th IEEE International Symposium on
 Volume 3, 1-4 Sept. 1997 Page(s):854 - 857 vol.3
 Digital Object Identifier 10.1109/PIMRC.1997.627007
[AbstractPlus](#) | Full Text: [PDF\(384 KB\)](#) IEEE CNF
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- 10. **Differential detection of $\pi/4$ -shifted-DQPSK for digital cellular radio**
 Chennakeshu, S.; Saulnier, G.J.;
Vehicular Technology Conference, 1991. 'Gateway to the Future Technology in Motion'.
IEEE
 19-22 May 1991 Page(s):186 - 191
 Digital Object Identifier 10.1109/VETEC.1991.140474
[AbstractPlus](#) | Full Text: [PDF\(452 KB\)](#) IEEE CNF
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- 11. **Applications of Lyapunov criteria in phase measurements using conformal mappi**
 Gundrum, H.C.; Rizkalla, M.E.;
Circuits and Systems, 1994., Proceedings of the 37th Midwest Symposium on
 Volume 2, 3-5 Aug. 1994 Page(s):1295 - 1298 vol.2
 Digital Object Identifier 10.1109/MWSCAS.1994.519046
[AbstractPlus](#) | Full Text: [PDF\(356 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 12. **A Low-Power Multirate Differential PSK Receiver for Space Applications**
 Yuce, M.R.; Liu, W.;
Vehicular Technology, IEEE Transactions on
 Volume 54, Issue 6, Nov. 2005 Page(s):2074 - 2084
 Digital Object Identifier 10.1109/TVT.2005.858196
[AbstractPlus](#) | Full Text: [PDF\(584 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- 13. **A direct-conversion receiver for 900 MHz (ISM band) spread-spectrum digital cord telephone**
 Hull, C.D.; Joo Leong Tham; Chu, R.R.;

[Solid-State Circuits, IEEE Journal of](#)
 Volume 31, Issue 12, Dec. 1996 Page(s):1955 - 1963
 Digital Object Identifier 10.1109/4.545818
[AbstractPlus](#) | [Full Text: PDF\(836 KB\)](#) IEEE JNL
[Rights and Permissions](#)

14. **Subharmonically pumped CMOS frequency conversion (up and down) circuits for WCDMA direct-conversion transceiver**
 Kwang-Jin Koh; Mun-Yang Park; Cheon-Soo Kim; Hyun-Kyu Yu;
[Solid-State Circuits, IEEE Journal of](#)
 Volume 39, Issue 6, June 2004 Page(s):871 - 884
 Digital Object Identifier 10.1109/JSSC.2004.827792
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(768 KB\)](#) IEEE JNL
[Rights and Permissions](#)
15. **Noncoherent detection of digitally phasor block-modulated signals in the presence of offset**
 Char-Dir Chung;
[Vehicular Technology Conference, 2004. VTC2004-Fall. 2004 IEEE 60th](#)
 Volume 3, 26-29 Sept. 2004 Page(s):2118 - 2122 Vol. 3
 Digital Object Identifier 10.1109/VETECF.2004.1400414
[AbstractPlus](#) | [Full Text: PDF\(675 KB\)](#) IEEE CNF
[Rights and Permissions](#)
16. **An adaptive direct conversion transmitter**
 Hilborn, D.S.; Stapleton, S.P.; Cavers, J.K.;
[Vehicular Technology, IEEE Transactions on](#)
 Volume 43, Issue 2, May 1994 Page(s):223 - 233
 Digital Object Identifier 10.1109/25.293640
[AbstractPlus](#) | [Full Text: PDF\(852 KB\)](#) IEEE JNL
[Rights and Permissions](#)
17. **BER performance analysis of a direct conversion receiver**
 Lindoff, B.; Malm, P.;
[Communications, IEEE Transactions on](#)
 Volume 50, Issue 5, May 2002 Page(s):856 - 865
 Digital Object Identifier 10.1109/TCOMM.2002.1006566
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(425 KB\)](#) IEEE JNL
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18. **A GFSK demodulator for low-IF Bluetooth receiver**
 Bo Xia; Chunyu Xin; Wenjun Sheng; Valero-Lopez, A.Y.; Sanchez-Sinencio, E.;
[Solid-State Circuits, IEEE Journal of](#)
 Volume 38, Issue 8, Aug. 2003 Page(s):1397 - 1400
 Digital Object Identifier 10.1109/JSSC.2003.814424
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(372 KB\)](#) IEEE JNL
[Rights and Permissions](#)
19. **Efficient compensation for frequency-dependent errors in analog reconstruction filters used in IQ modulators**
 Tuthill, J.; Cantoni, A.;
[Communications, IEEE Transactions on](#)
 Volume 53, Issue 3, March 2005 Page(s):489 - 496
 Digital Object Identifier 10.1109/TCOMM.2005.843455
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(384 KB\)](#) IEEE JNL
[Rights and Permissions](#)
20. **A new method of an IF I/Q demodulator for narrowband signals**
 Xue, R.; Xu, Q.; Chang, K.F.; Tam, K.W.;
[Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on](#)
 23-26 May 2005 Page(s):3817 - 3820 Vol. 4
 Digital Object Identifier 10.1109/ISCAS.2005.1465462

[AbstractPlus](#) | Full Text: [PDF\(480 KB\)](#) IEEE CNF
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21. **Good and bad training sequences for zero IF sampling edge receivers**
 Krueger, M.; Denk, R.; Yang, B.;
[Acoustics, Speech, and Signal Processing, 2004. Proceedings. \(ICASSP '04\). IEEE International Conference on](#)
 Volume 4, 17-21 May 2004 Page(s):iv - 1033-6 vol.4
 Digital Object Identifier 10.1109/ICASSP.2004.1327006
[AbstractPlus](#) | Full Text: [PDF\(260 KB\)](#) IEEE CNF
[Rights and Permissions](#)
22. **Improving QPSK demodulator performance for quadrature receiver with information amplitude and phase imbalance correction**
 Hung Nguyen;
[Wireless Communications and Networking Conference, 2000. WCNC. 2000 IEEE](#)
 Volume 3, 23-28 Sept. 2000 Page(s):1440 - 1444 vol.3
 Digital Object Identifier 10.1109/WCNC.2000.904844
[AbstractPlus](#) | Full Text: [PDF\(332 KB\)](#) IEEE CNF
[Rights and Permissions](#)
23. **Using a direct conversion receiver in EDGE terminals-a new DC offset compensation algorithm**
 Lindoff, B.;
[Personal, Indoor and Mobile Radio Communications, 2000. PIMRC 2000. The 11th IEEE International Symposium on](#)
 Volume 2, 18-21 Sept. 2000 Page(s):959 - 963 vol.2
 Digital Object Identifier 10.1109/PIMRC.2000.881564
[AbstractPlus](#) | Full Text: [PDF\(324 KB\)](#) IEEE CNF
[Rights and Permissions](#)
24. **Effect of DC offset on performance of differentially detected $\pi/4$ DQPSK**
 Huang, S.; Stonick, J.T.;
[Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on](#)
 Volume 4, 28-31 May 2000 Page(s):757 - 760 vol.4
 Digital Object Identifier 10.1109/ISCAS.2000.858862
[AbstractPlus](#) | Full Text: [PDF\(288 KB\)](#) IEEE CNF
[Rights and Permissions](#)
25. **40 Mbit/s adaptive MLSE equalizer LSI and its performance in 5 GHz-band transmission system**
 Shirato, Y.; Kobayashi, K.; Denno, S.;
[Vehicular Technology Conference, 1999 IEEE 49th](#)
 Volume 1, 16-20 May 1999 Page(s):305 - 308 vol.1
 Digital Object Identifier 10.1109/VETEC.1999.778066
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